Table 1 – LCD Home Bottom Line Test Plan

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| **Test Writer:** | | Steven Taylor |  |  |  |  |
| **Test Case Name:** | | Home Bottom Line | **Test ID #:** |  |  | Bottom Line Homing Test |
| **Description:** | | Verify the operation of the library function to Home the bottom line of the LCD | **Type:** | |  | White Box |
| **X** | Black Box |
| **Tester Information** | |  |  |  |  |  |
| **Name of Tester:** | |  | Date: |  |  |  |
| **Hardware Ver:** | | 1.0 | Time: |  |  |  |
| **Setup:** | Connect STK600 Port\_K\_ to the LCD board input pins, and connect the LCD board to power. Run homing test code | | | | | |
| **Test** | **(Test Variable)** | **Expected Result** | **Pass** | **Fail** | **N/A** | **Comments** |
| 1 | Home Bottom | Logic Analyzer shows 0b0010000 for 0.39us ±5% |  |  |  |  |
| **Overall Test Result:** | |  |  |  |  |  |